CLAIMS

What is claimed is:

1. A control circuit for an optical record medium processing device mounted in a computer host, the computer host having a power supply capable of converting an alternating current to a direct current and outputting the direct current at both power-on and power-off statuses of the computer host, such that the optical record medium processing devicen operates at both power-on and power-off statuses of the computer host, the control circuit comprising:

a command generator unit, distributing an operation signal regardless of whether the computer host is at a power-on or power-off status;

a logic unit, coupled to the command generator unit through a system management bus to receive the operation signal and distribute a control signal in response to the operation signal;

a power supply control chip, respectively coupled with the command generator unit and the logic unit to provide required power for a normal operation of the command generator unit and the logic unit at a power-off status of the computer host;

wherein the optical record medium processing device automatically responds to the control signal and works correspondingly at both powered-on and power-off statuses of the computer host.

2. The control circuit of claim 1, wherein the logic unit is a south bridge chip.

3. The control circuit of claim 1, wherein the command generator unit at least includes a micro-processor.

4. The control circuit of claim 3, wherein the command generator unit further includes a signal receiver unit for receiving a wireless signal.

5. The control circuit of claim 4, wherein the wireless signal is transmitted by a wireless signal generator unit.

6. A method of controlling an optical record medium processing device, suitable for a computer host, the computer host having a power supply capable of converting an alternating current to a direct current and outputting the direct current at both power-on and power-off statuses of the computer host to enable a normal operation of the optical record medium processing device, the method comprising:

receiving an operation signal by a command generator unit;

determining the power supply status of the computer host and distributing a control signal in response to the operation signal;

outputting a control signal to the optical record medium processing device from a logic unit, thereby the optical record medium processing device automatically responds to the control signal and works correspondingly.

7. The method of claim 6, wherein if the computer host is at the power-on status in the step of determining the power supply status, then further generating an interrupt signal and storing the current task status of the computer host.

8. The method of claim 6, wherein if the host is at a power-off status in the step of determining the power supply status, then BIOS of the computer host distributes the control signal.

9. The method of claim 6, wherein when the host is at a power-off status, the control signal is sent from the command generator unit.